

Application No.: 10/055,545
Docket No.: JCLA6997

In The Claims

Please amend claims 1, 7 and 10-16 as follows:

Claim 1. (Currently amended) A multi-domain vertical alignment liquid crystal display, comprising at least:

9, a first substrate, having a plurality of thin-film transistors, a plurality of protrusions and a plurality of pixel electrodes thereon, wherein the pixel electrodes formed over ~~on~~ the protrusions have a plurality of slits, whereby the protrusions and the slits produce a multi-domain mechanism, and the first substrate further includes a planarized dielectric layer on the protrusions and the slits;

a second substrate; and

a liquid crystal layer disposed between the first substrate and the second substrate.

Claim 2. (original) The multi-domain vertical alignment liquid crystal display according to claim 1, wherein the first and second substrates include glass substrates.

Claim 3. (original) The multi-domain vertical alignment liquid crystal display according to claim 1, wherein the first substrate includes a thin-film transistor array substrate.

Claim 4. (original) The multi-domain vertical alignment liquid crystal display according to claim 1, wherein the second substrate has a plurality of black matrices and color filters.

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Claim 5. (original) The multi-domain vertical alignment liquid crystal display according to claim 1, wherein extension directions of the protrusions and the slits are parallel to each other.

Claim 6. (original) The multi-domain vertical alignment liquid crystal display according to claim 1, wherein extension directions of the protrusions and the slits are not parallel to each other.

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Claim 7. (currently amended) The multi-domain vertical alignment liquid crystal display ~~method~~ according to claim 1, wherein the protrusions and the slits are alternately arranged.

Claim 8. (original) The multi-domain vertical alignment array liquid crystal display according to claim 1, wherein the pixel electrodes on the protrusions are exposed while the dielectric layer is planarized.

Claim 9. (original) The multi-domain vertical alignment array liquid crystal display according to claim 1, wherein the dielectric layer covers the pixel electrodes on the protrusions with a thickness thinner than that of the dielectric layer in other positions.

Claim 10. (currently amended) A thin-film transistor array substrate, comprising:
a substrate;
a plurality of thin-film transistors formed on the substrate to provide an electric field;

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a plurality of pixel electrodes covering the protrusions and having a plurality of slits ~~alternately~~ arranged with the protrusions to form multiple domains; and

a dielectric layer, covering the pixel electrodes and the slits, the dielectric layer having a planarized surface.

Claim 11. (currently amended) The thin-film transistor array substrate multi-domain vertical alignment liquid crystal display according to claim 10, wherein the thin-film transistor array substrate includes a glass substrate.

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Claim 12. (currently amended) The thin-film transistor array substrate multi-domain vertical alignment liquid crystal display according to claim 10, wherein extension directions of the protrusions and the slits are parallel to each other.

Claim 13. (currently amended) The thin-film transistor array substrate multi-domain vertical alignment liquid crystal display according to claim 10, wherein extension directions of the protrusions and the slits are not parallel to each other.

Claim 14. (currently amended) The thin-film transistor array substrate multi-domain vertical alignment liquid crystal display according to claim 10, wherein protrusions and the slits are alternately arranged with each other.

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Claim 15. (currently amended) The thin-film transistor array substrate multi-domain
~~vertical alignment liquid crystal display~~ according to claim 10, wherein the pixel electrodes on
the protrusions are exposed.

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Claim 16. (currently amended) The thin-film transistor array substrate multi-domain
~~vertical alignment liquid crystal display~~ according to claim 10, wherein the dielectric layer
covers the pixel electrodes on the protrusions with a thickness thinner than the dielectric layer in
other positions.
